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CHICAGO, IL 60601

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Nanette W. Combs	(Depositor's name)
<i>Nanette Combs</i>	(Signature)
5/4/2005	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/688,550	10/17/2003	Jonggook Kim	P05779	1147

TITLE OF INVENTION: METHOD FOR QUANTIFYING SAFE OPERATING AREA FOR BIPOLAR JUNCTION TRANSISTOR

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$1400	07/06/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
LE, JOHN H	2863	702-117000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).  
☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.  
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2. For printing on the patent front page, list  
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## 3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

National Semiconductor Corporation

Santa Clara, California

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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## 5. Change in Entry Status (from status indicated above)

- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature: *Mark A. Dalla Valle*Date: May 4, 2005Typed or printed name Mark A. Dalla ValleRegistration No. 34,147

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Name: Mail Stop Issue Fee  
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### Message:

U.S. Patent Application No. 10/688,550, filed October 17, 2003 Docket No. P05779  
(11461.00.5779)

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*Nanette Combs*

Nanette W. Combs